

**Amendments to the Specification:**

Please amend the specification by inserting the paragraph below, after the title on page 1.

**Cross-Reference to Related Application**

This application claims priority based on International Patent Application No. PCT/FR2003/050173, entitled "MIS Transistor with Self-Aligned Gate and Method for Making Same" by Simon DELEONIBUS, which claims priority of French Application No. 02 15916, filed on December 16, 2002, and which was not published in English.